

FIG. 1

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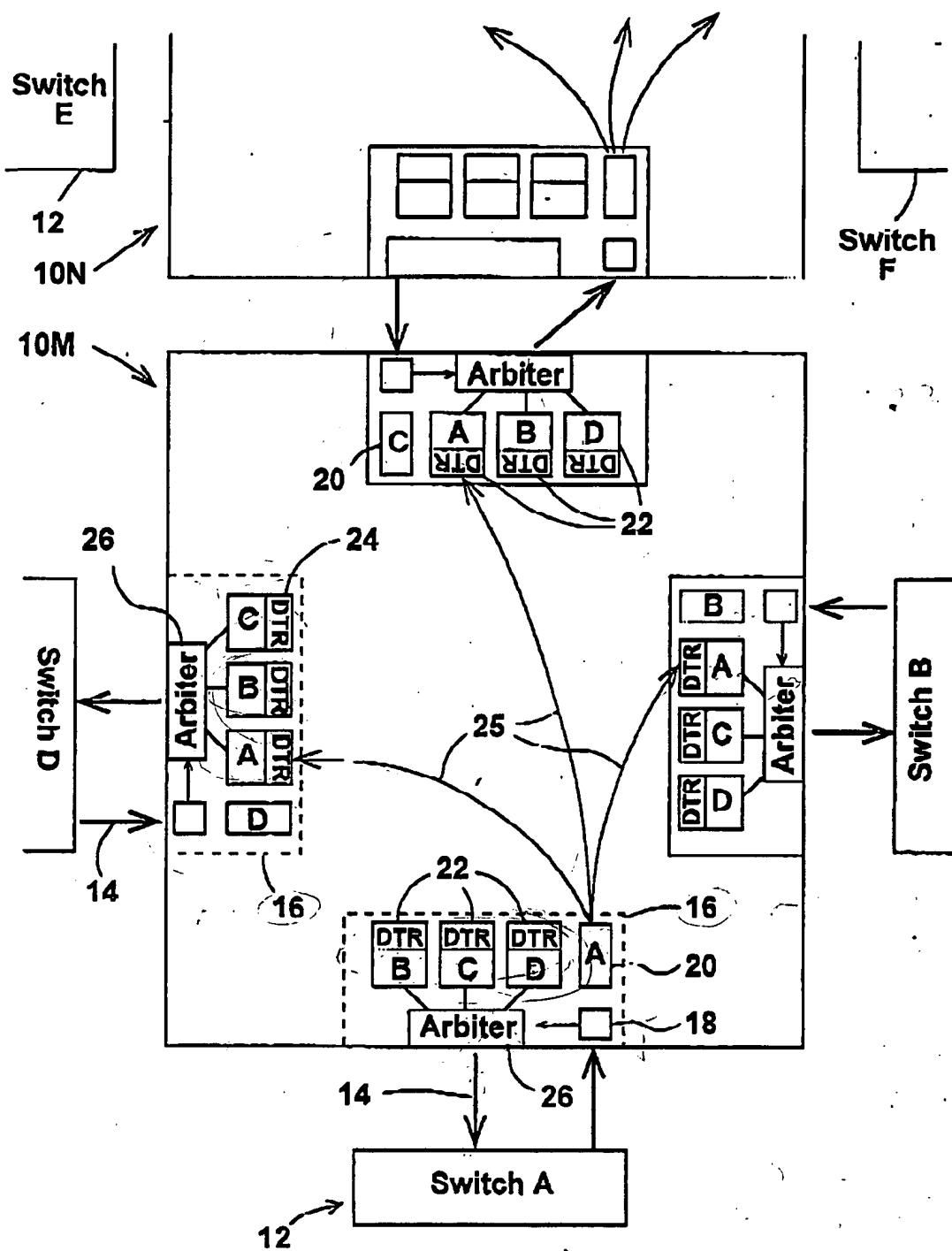


FIG. 2

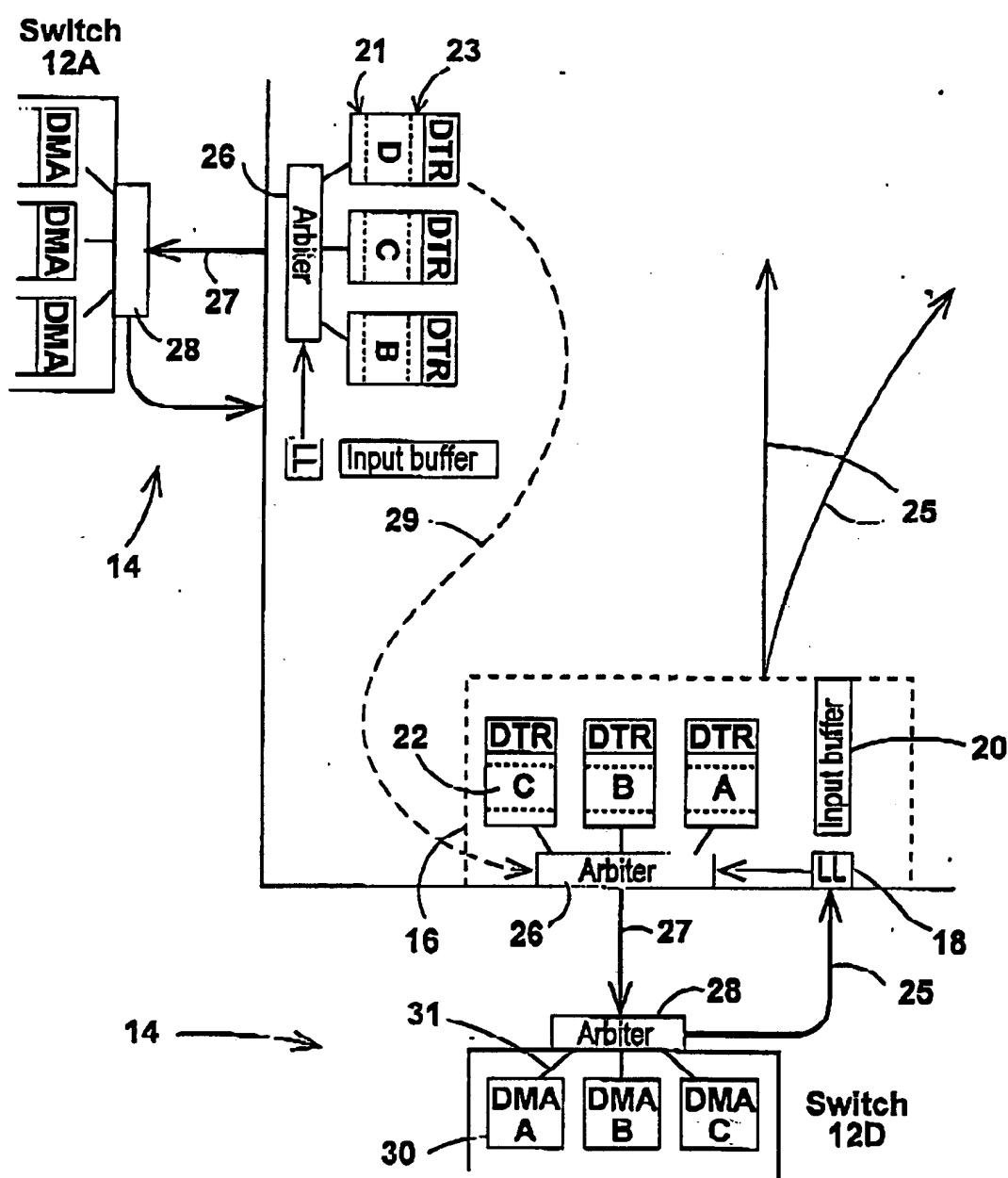
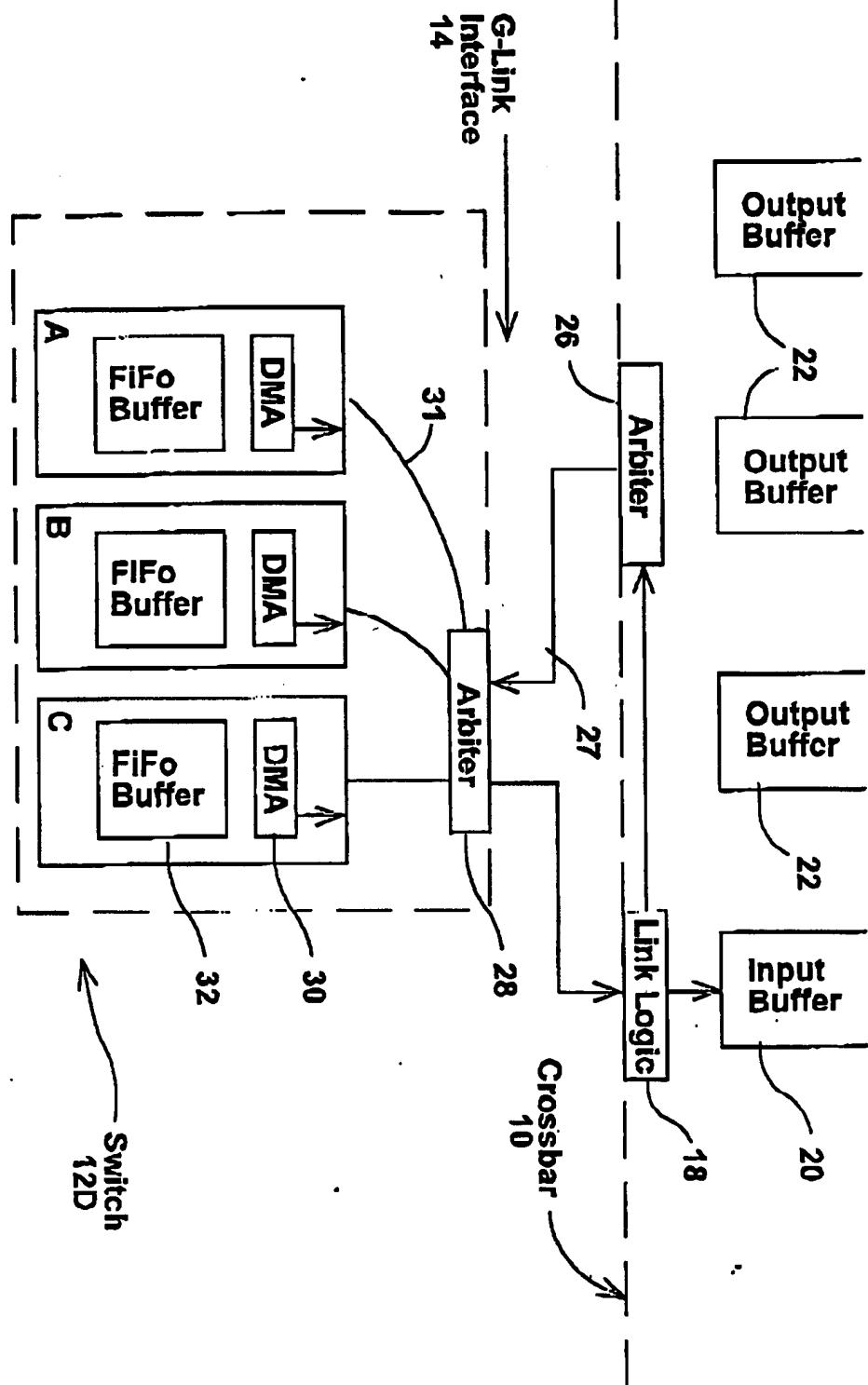
**FIG. 3**

FIG. 4

0 9 3 4 8 8 6 5 . 0 7 0 7 9 9

Source Network	Port 16A on	16B on	Destination
Switch 12A	Cross Bar 10	Cross Bar 10	Network
			Switch 12B

110-Transfer a link message and data packet

**112-Logic Unit
18A identifies link message as switch link message**

114-Write link message and data packet into Input Buffer 20A

116-Transfer link message and data packet to Registrar 24A to Ports 16B, 16C and 16D

118-Receive message and data at registrars 24A at ports 16B, 16C and 16D

120-Message and data not recognize and ignore by registrars 24A at ports 16C and 16D

124-Write message and data into output buffer 22A

122-Message and data recognize by registrar 14A at ports 16B

126-Transfer data from output buffer 22B to cross bar arbiter 26B

128-Transfer data from cross bar arbiter 26B via link 14B to switch 12B

FIG. 5A

Source Network Switch 12A	Port 16A on Cross Bar 10	16B on Cross Bar 10	Destination Network Switch 12B
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FIG. 5B

- 130-Switch 12B full.
Transfer local link message to logic unit 18B
- 132-Logic unit 18B indicates to cross bar arbiter 26B to cut off incoming data to switch 12B
- 134-Output buffer 22A temporarily stores unsendable data
- 136-Output buffer 22A reaches "fullness"
- 138-Output buffer 22A notifies cross bar arbiter 26A of full status
- 140-Cross bar 26A notifies switch arbiter 28A at switch A not to send more data
- 142-DMA 30B stops sending to switch 12B
- 143-Switch buffer 32B temporarily stores messages and data
- 144-Switch 12B less full.
Transfer local link message to logic unit 18B

Source Network Switch 12A	Port 16A on Cross Bar 10	16B on Cross Bar 10	Destination Network Switch 12B
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146-Logic unit
18B indicates to
cross bar arbiter
26B to reopen
inflow to
switch 12B

148-Output
buffer 22A restarts
transmission to
switch 12B

150-Output
buffer 22A reaches
emptiness, notifies
cross bar arbiter 26A

152-Cross bar
arbiter 28A notifies
switch arbiter 26A
to resume
transfer

154-Switch 12A
indicates to DMA
30B to resume
communication

156-Switch buffer 32B
resumes sending
data

FIG. 5B Continued

G - Link Interface

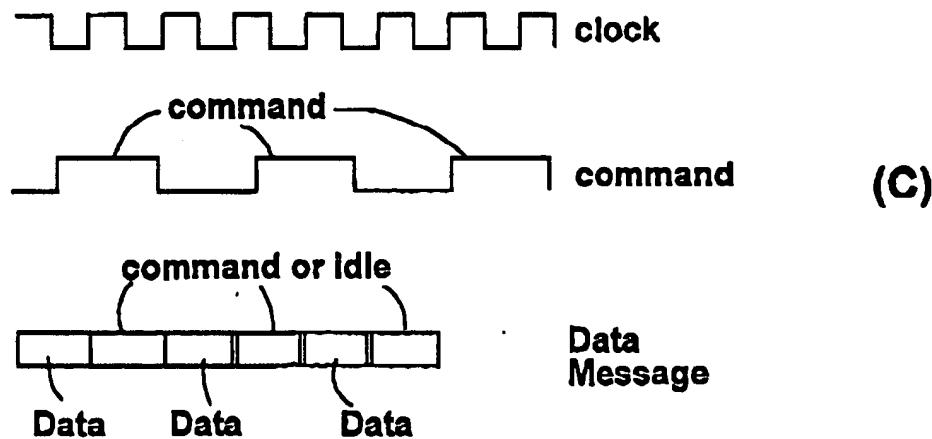
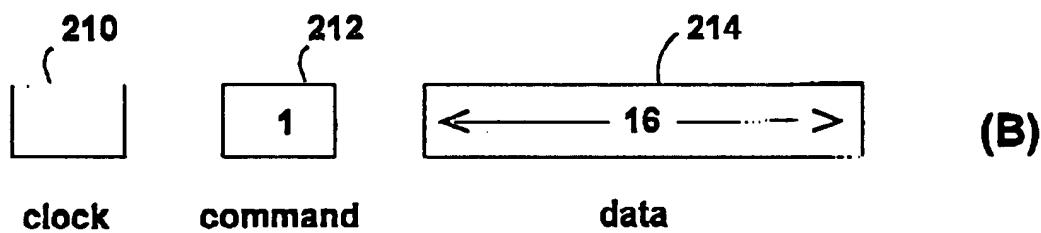
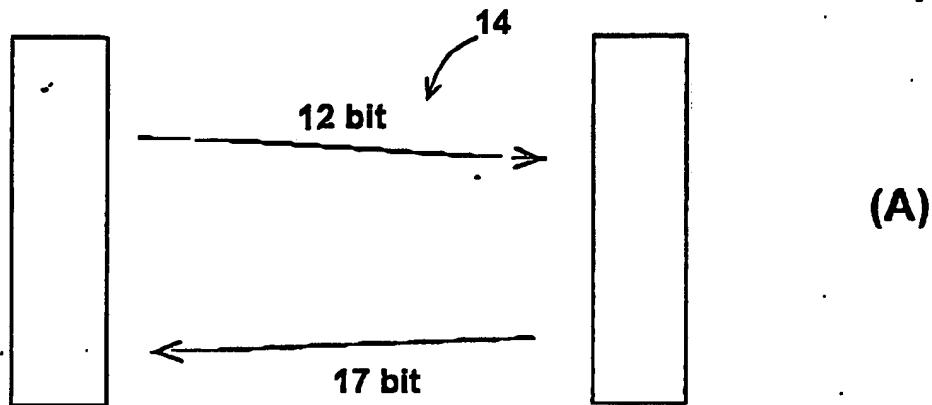
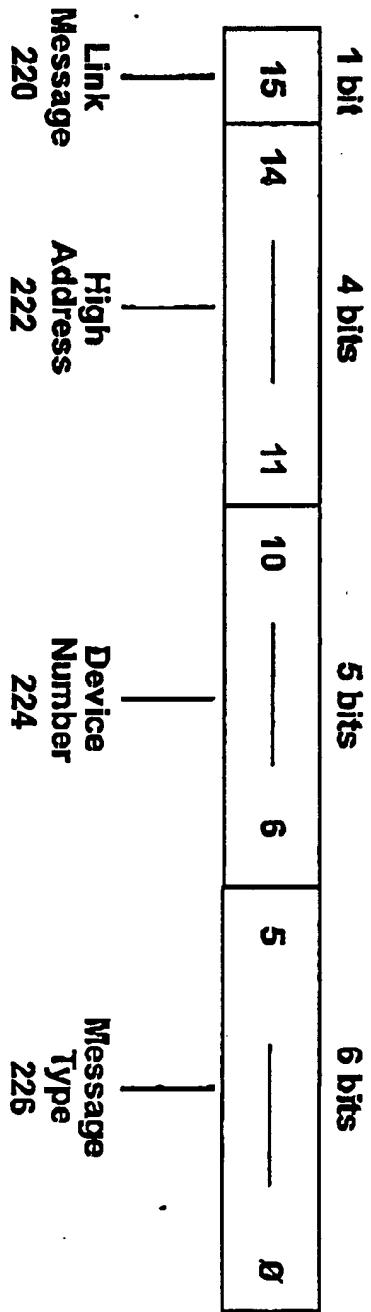


FIG. 6

Data Message**FIG. 7**

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